

U.S. Department of Commerce, Patent and Trademark Office						Atty Docket No.	Serial No.
						M-7413 US	09/503,552
INFORMATION DISCLOSURE STATEMENT BY APPLICANT						Applicants	
(Use several sheets if necessary)						Epps, Garry P.; Laor, Michael	
						Filing Date	Group
						02/14/2000	Unassigned
U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
Foreign Patent Documents							
							Translation
		Document	Date	Country	Class	Subclass	Yes No
	AL						
	AM						
	AN						
	AO						
	AP						
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
	AQ	M. Shreedhar and G. Varghese, "Efficient Fair Queuing Using Deficit Round Robin," Proceedings of ACM SIG COMM '95, Computer Communications Review, Vol. 25, No. 4, October 1995.					
	AR	S. Floyd and V. Jacobson, "Random Early Detection Gateways for Congestion Avoidance", IEEE/ACM Transactions on Networking, ACM Press, August 1993.					
	AS						
Examiner			Date Considered	8/23/04			
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.							

U.S. Department of Commerce, Patent and Trademark Office						Atty. Docket No.		Serial No.	
						CIS0021US		09/503,552	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT						Applicant(s)			
(Use several sheets if necessary)						Garry P. Epps and Michael Laor			
						Filing Date		Group	
						February 14, 2000		2739	

U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	5,790,545	Aug. 4, 1998	Holt et al.	370	418	
	AB	5,909,686	July 1, 1999	Muller et al.	707	104	
	AC	6,038,230	Mar. 14, 2000	Ofek	370	389	
	AD	6,038,607	Mar. 14, 2000	Hamilton et al.	709	236	
	AE	6,147,996	Nov. 14, 2000	Laor et al.	370	394	
	AF	6,167,445	Dec. 26, 2000	Gai et al.	709	223	
	AG	6,226,267	May 1, 2001	Spinney et al.	370	235	
	AH	6,259,695	July 10, 2001	Ofek	370	389	
	AI	6,259,699	July 10, 2001	Opalka et al.	370	398	
	AJ	6,343,072	Jan. 29, 2002	Bechtolsheim et al.	370	351	
	AK	6,389,506	May 14, 2002	Ross et al.	711	108	

Foreign Patent Documents							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
	AQ	
	AR	
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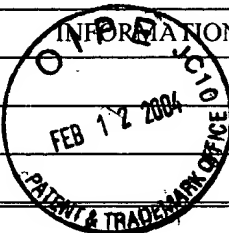
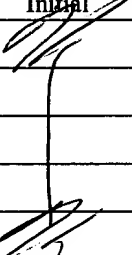


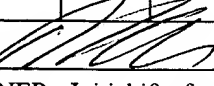
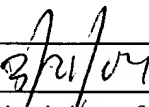
Examiner	Date Considered 3/24/04
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					Applicant(s)			
(Use several sheets if necessary)					Garry P. Epps and Michael Laor			
					Filing Date		Group	
					February 14, 2000		2739	
U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA	6,396,809	May 28, 2002	Holden et al.	370	236		
	AB	6,404,752	June 11, 2002	Allen, Jr. et al.	370	335		
	AC	6,424,649	July 23, 2002	Laor et al.	370	359		
	AD	6,424,659	Jul. 23, 2002	Viswanadham et al.	370	469		
	AE	6,463,059	Oct. 8, 2002	Movshovich et al.	370	389		
	AF							
	AG							
	AH							
	AI							
	AJ							
	AK							
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	AM							
	AN							
	AO							
	AP							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
	AQ	Sally Floyd and van Jacobson, <i>Random Early Detection Gateways for Congestion Avoidance</i> , IEEE/ACM Transactions on Networking, Vo. 1, No. 4, August 1993, pgs. 397-413.						
	AR	M. Shreedhar and George Varghese, <i>Efficient Fair Queuing Using Deficit Round Robin</i> , Computer Communication Review, Vol. 25, No. 4, October 1995, pgs. 231-242.						
	AS							
Examiner 		Date Considered 						
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